### Application Data Sheet

## Application Information

Application Type::

Regular

Subject Matter::

Utility

Title::

Mixed Stack-Based RISC Processor

Attorney Docket Number::

TI-35433 (1962-05412)

Request for Early Publication?:: Request for Non-Publication?::

No 2

No

Suggested Drawing Figure:: Total Drawing Sheets::

5

Small Entity?::

No

### Applicant Information

Applicant Authority type::

Inventor

Primary Citizenship Country::

France

Status::

Full Capacity

Given Name::

Gerard

Family Name::

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Country of Residence::

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Postal or Zip Code of

mailing address::

06600

Applicant Authority type::

Inventor France

Primary Citizenship Country::

Full Capacity

Status::

Serge

Given Name::

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LASSERRE

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Applicant Authority type:: Inventor Primary Citizenship Country:: Belgium

Status:: Full Capacity

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Country of mailing address:: France

Postal or Zip Code of

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Correspondence Information

Correspondence Customer Number:: 23494

Representative Information

Representative Customer Number:: 23494

#### DOMESTIC PRIORITY INFORMATION

Application::	Continuity	Parent	Parent Filing
	Type::	Application::	Date::
This	Provisional of	60/400,391	07/31/02
Application			

### FOREIGN PRIORITY INFORMATION

Country::	Application	Filing Date::	Priority
	number::		Claimed::
EPO	03291916.9	July 30, 2003	YES

# Assignee Information

Assignee name:: Texas Instruments Incorporated

Street of mailing address:: P.O. Box 655474, MS 3999

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mailing address:: TX

Country of mailing

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